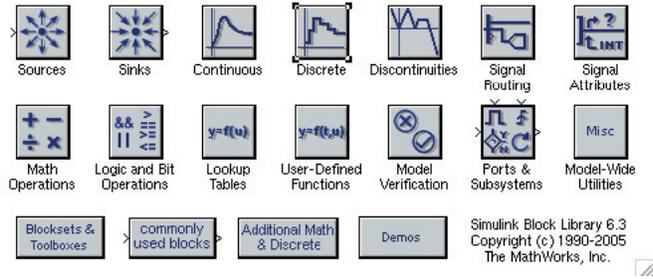
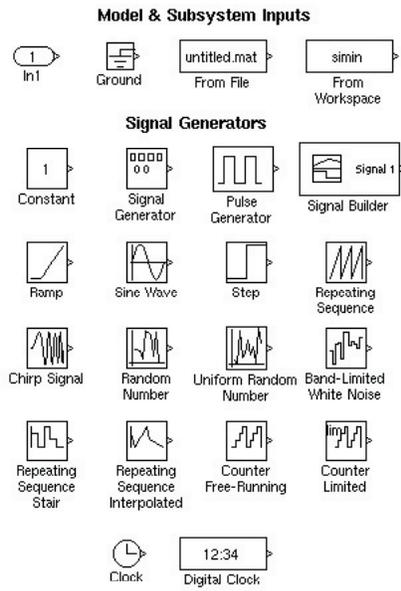


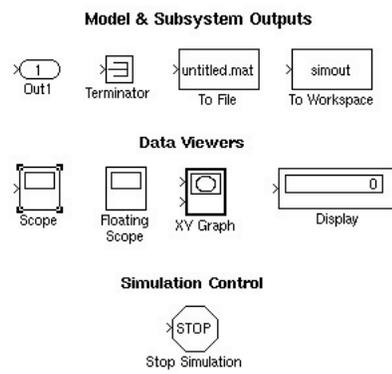
The Simulink Block Sets



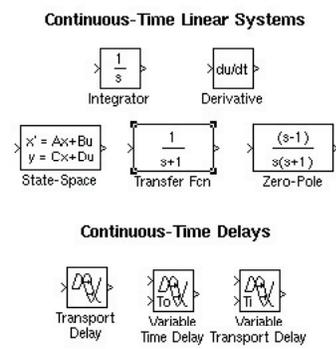
Sources



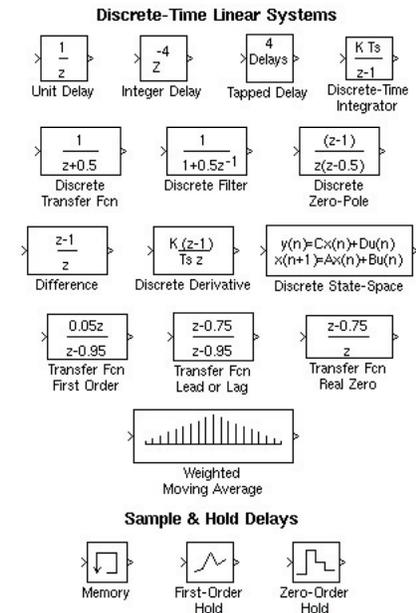
Sinks



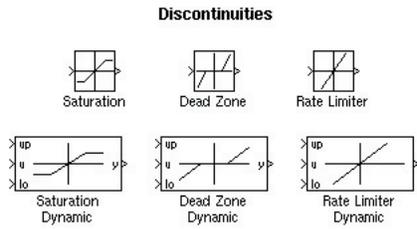
Continuous



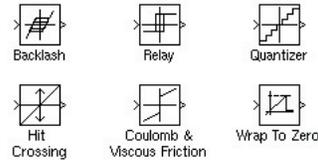
Discrete



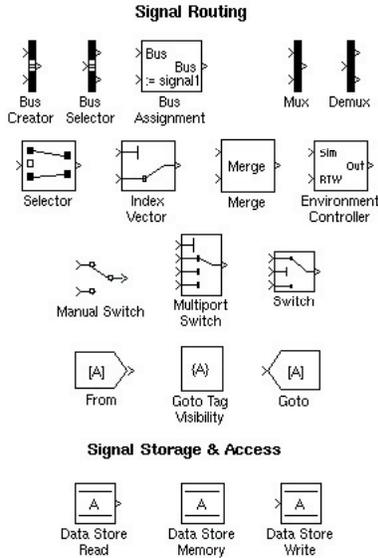
Discontinuities



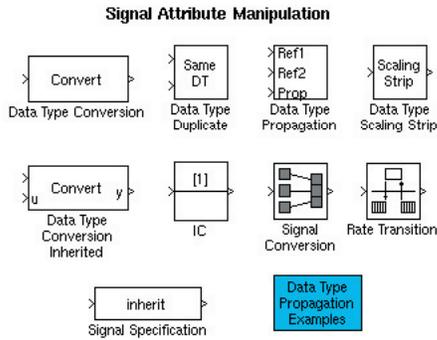
Discontinuities (cont.)



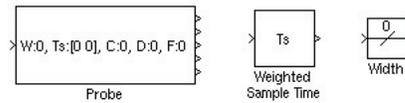
Signal Routing



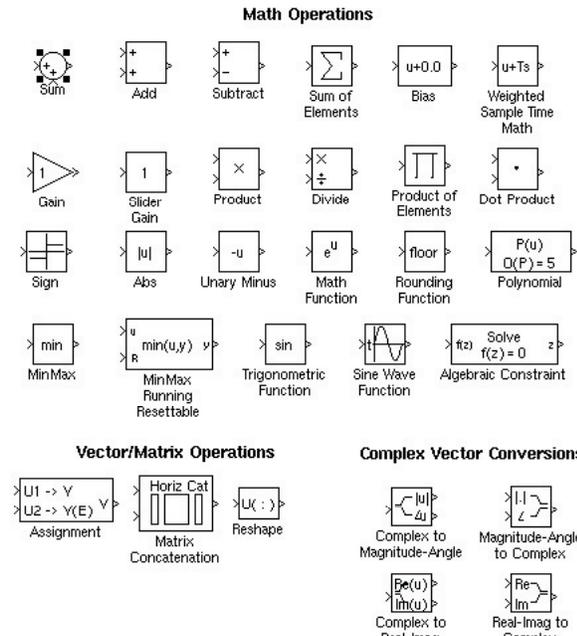
Signal Attributes



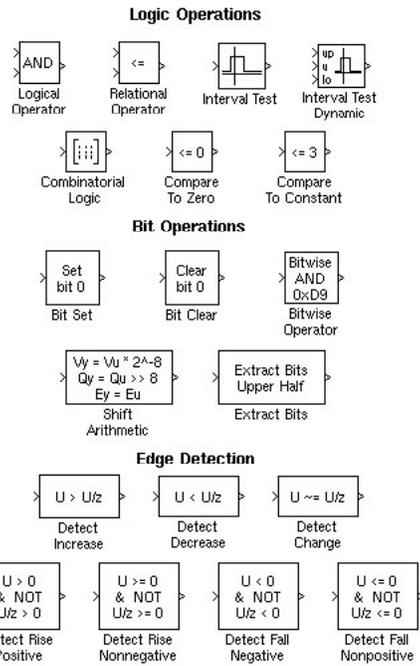
Signal Attribute Detection



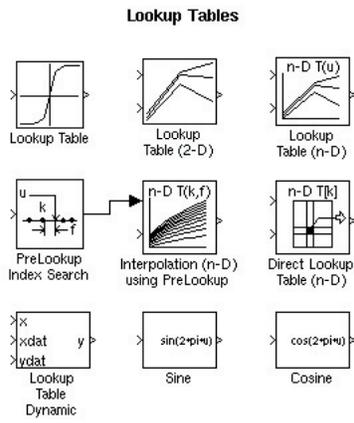
Math Operations



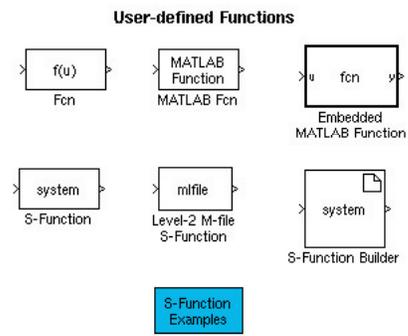
Logic and Bit Operations



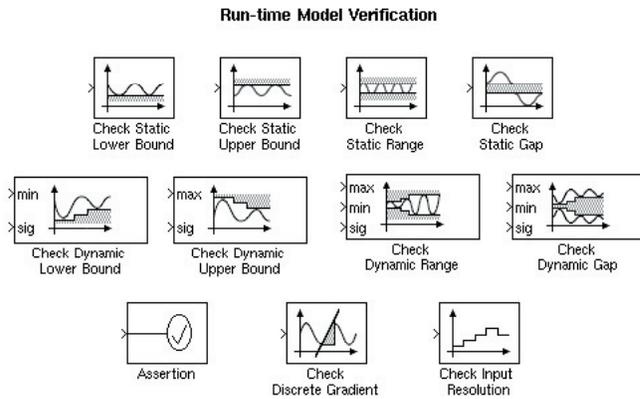
Lookup Tables



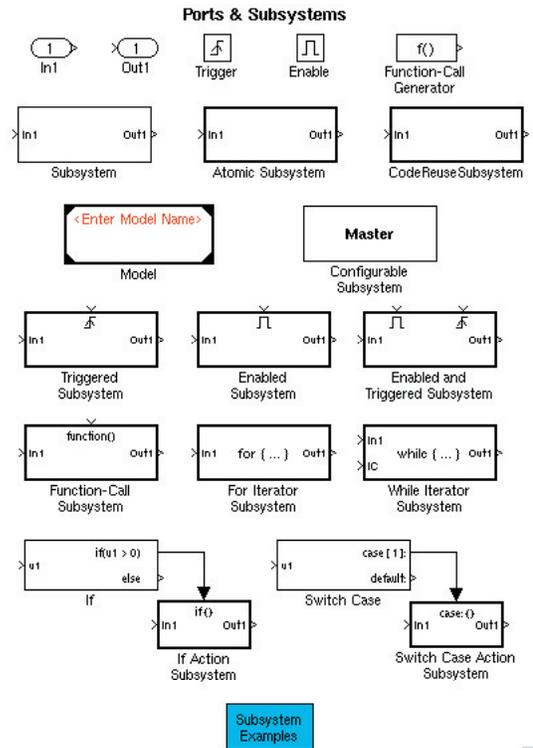
User Defined Functions



Model Verification

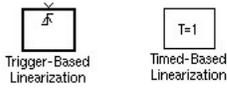


Ports and Subsystems

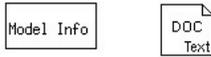


Misc/Model Wide Utilities

Linearization of Running Models



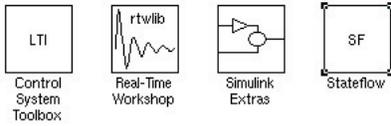
Documentation



Modeling Guides

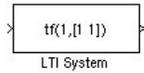


Blocksets & Toolboxes

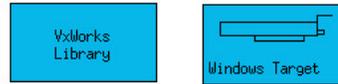


B&T: Control Systems Toolbox

Simulink blocks for use with the Control System Toolbox

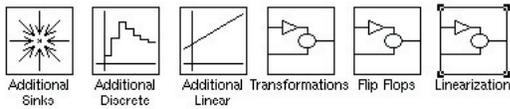


B&T: Real-Time Workshop

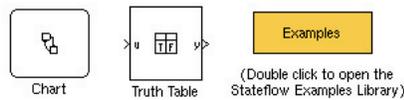


Real-Time Workshop 6.3
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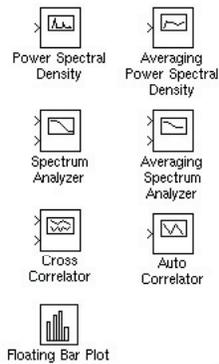
B&T: Simulink Extras



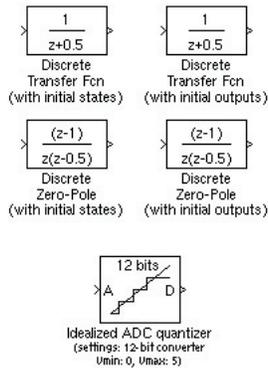
B&T: Stateflow



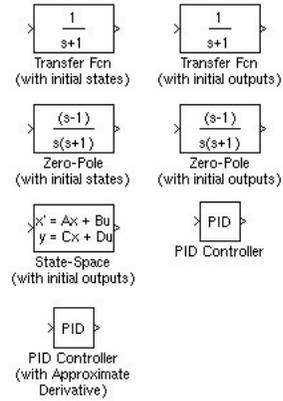
B&T: S E: Additional Sinks



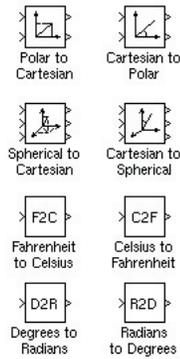
B&T: S E: Additional Discrete



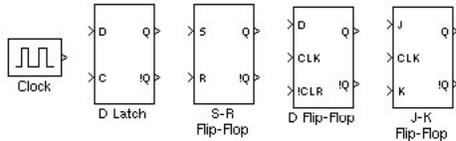
B&T: S E: Additional Linear



B&T: S E: Transformations



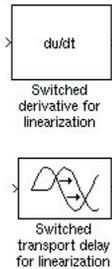
B&T: S E: Flip-Flops



Note: J-K Flip-Flop is Negative-Edge-Triggered

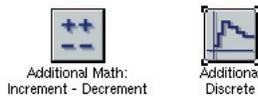
B&T: S E: Linearization

Block for use with linmod:



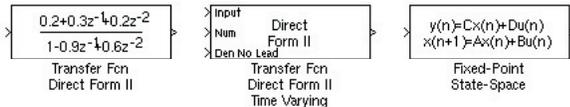
Additional Math and Discrete

Additional Math & Discrete



AM&D: Additional Math

Additional Discrete



AM&D: Increment - Decrement

Increment/Decrement

